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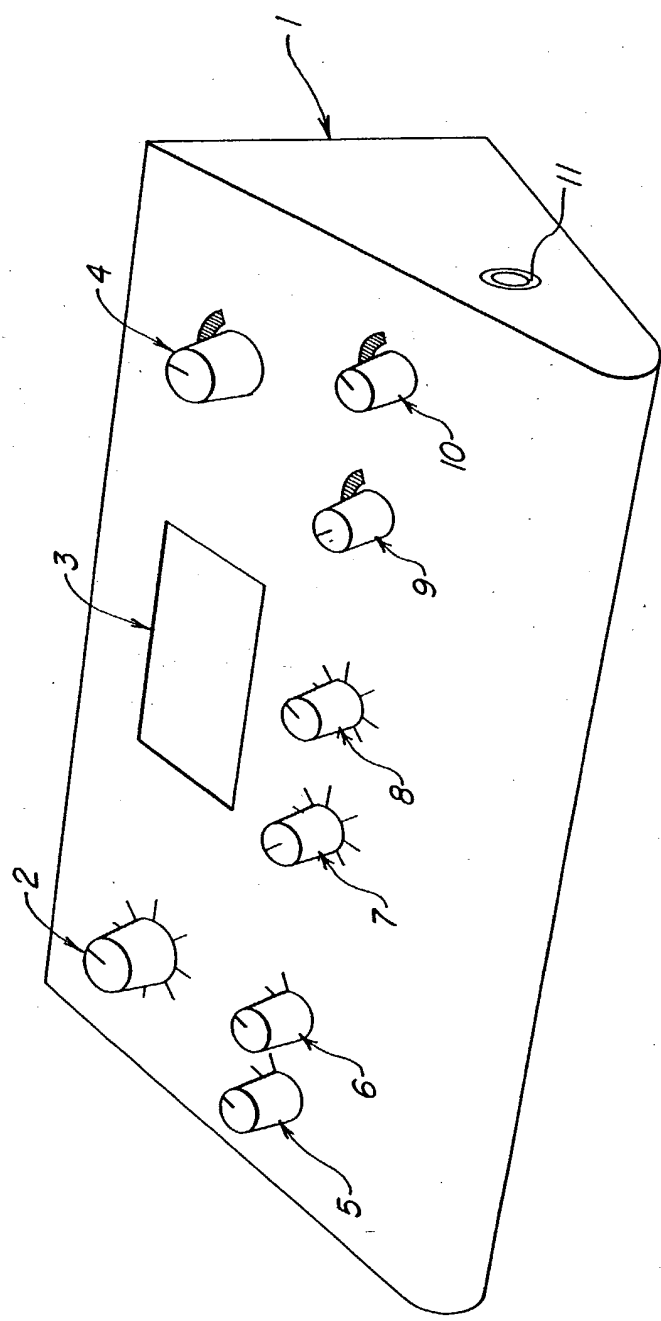
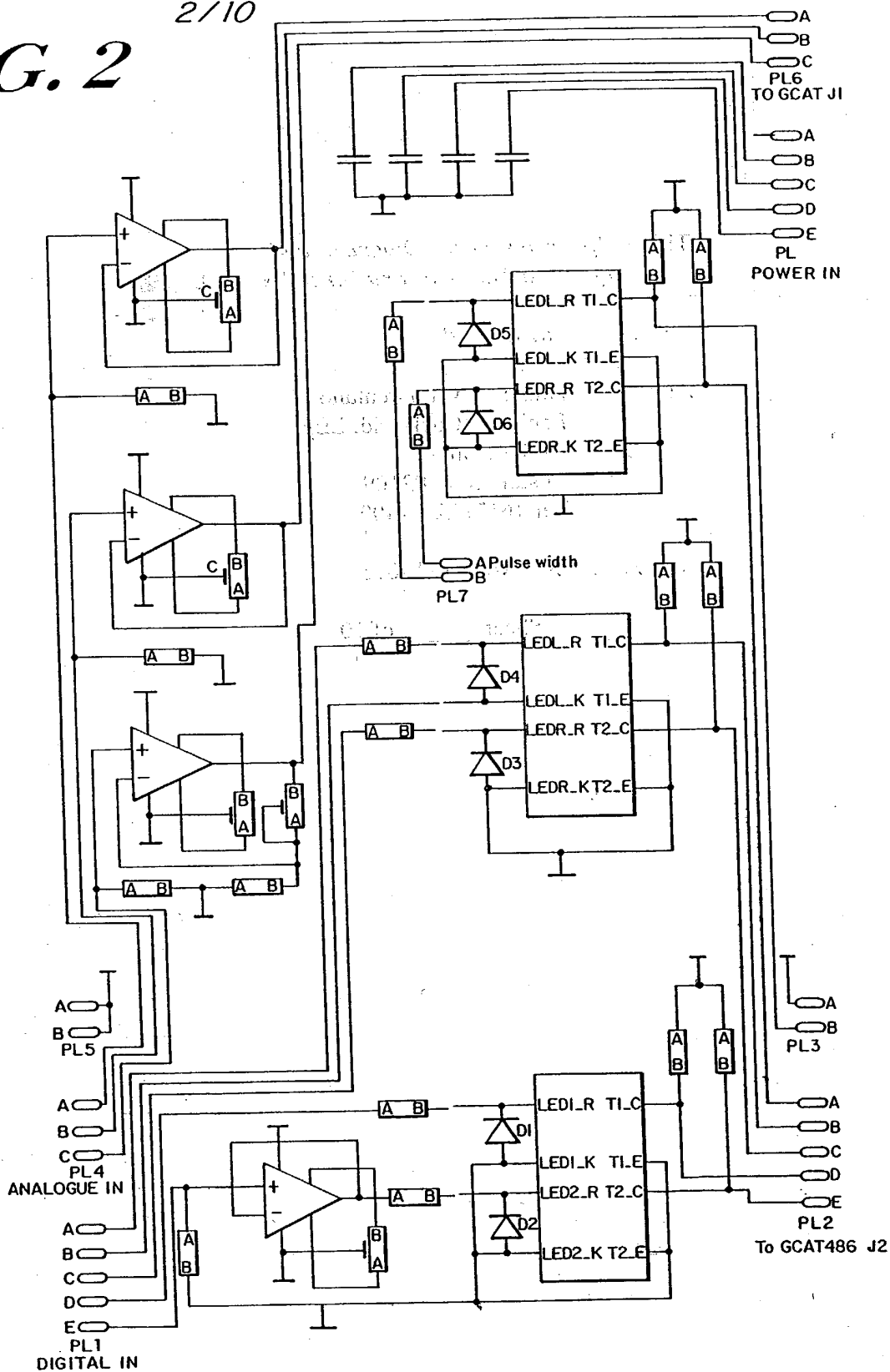


FIG. 1

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FIG. 2



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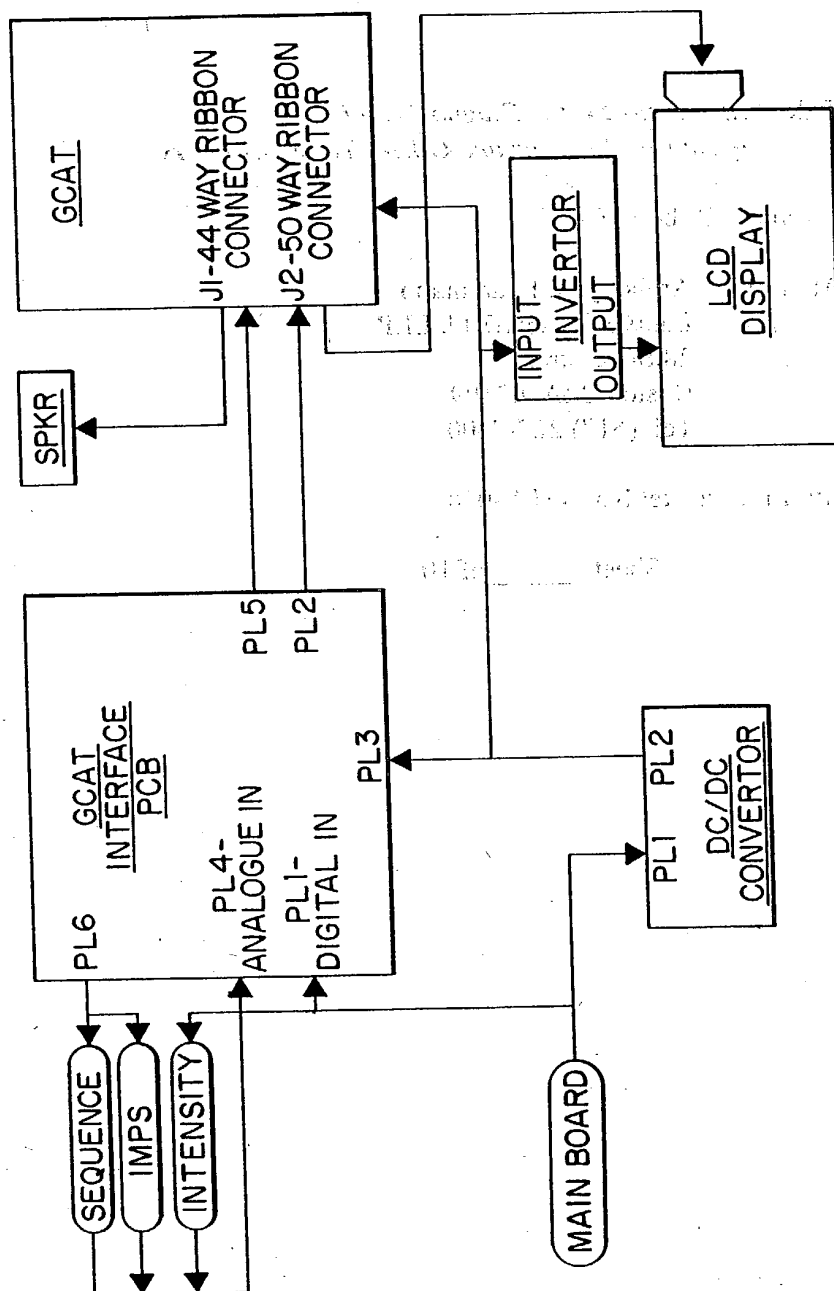
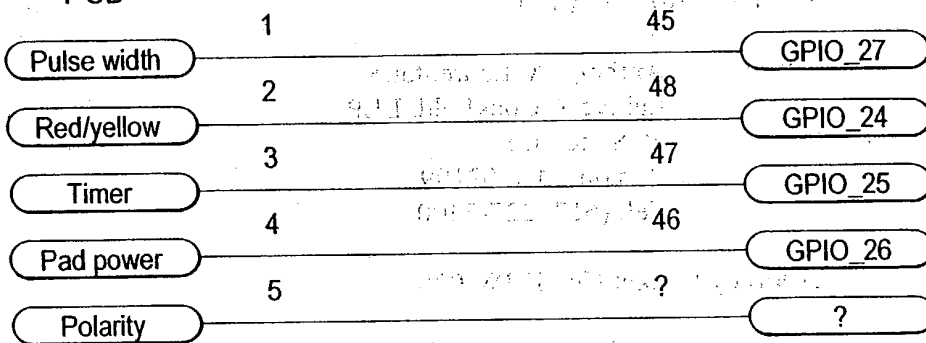
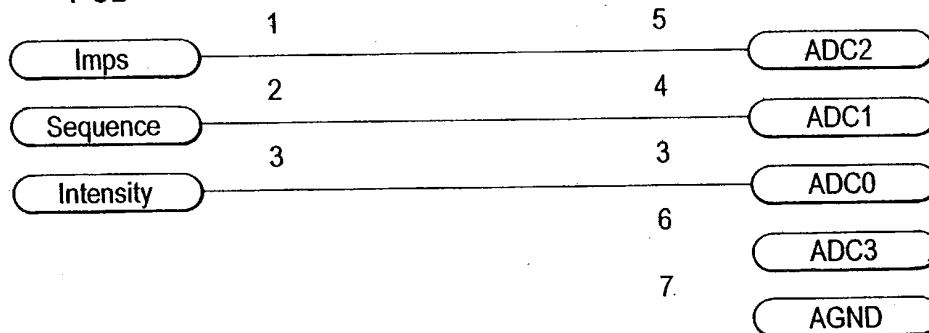


FIG. 3A

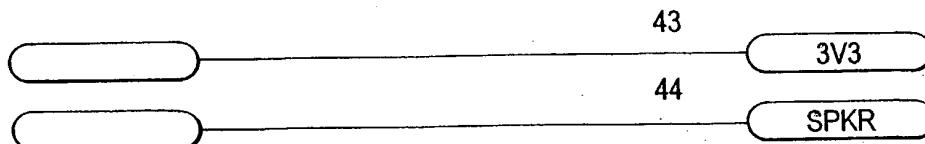


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MAIN BOARD  
PCBPL1  
DC/DC  
CONVERTERPL2  
INTERFACE  
PCBGCAT  
J2PL5  
INTERFACE  
PCBGCAT  
J1

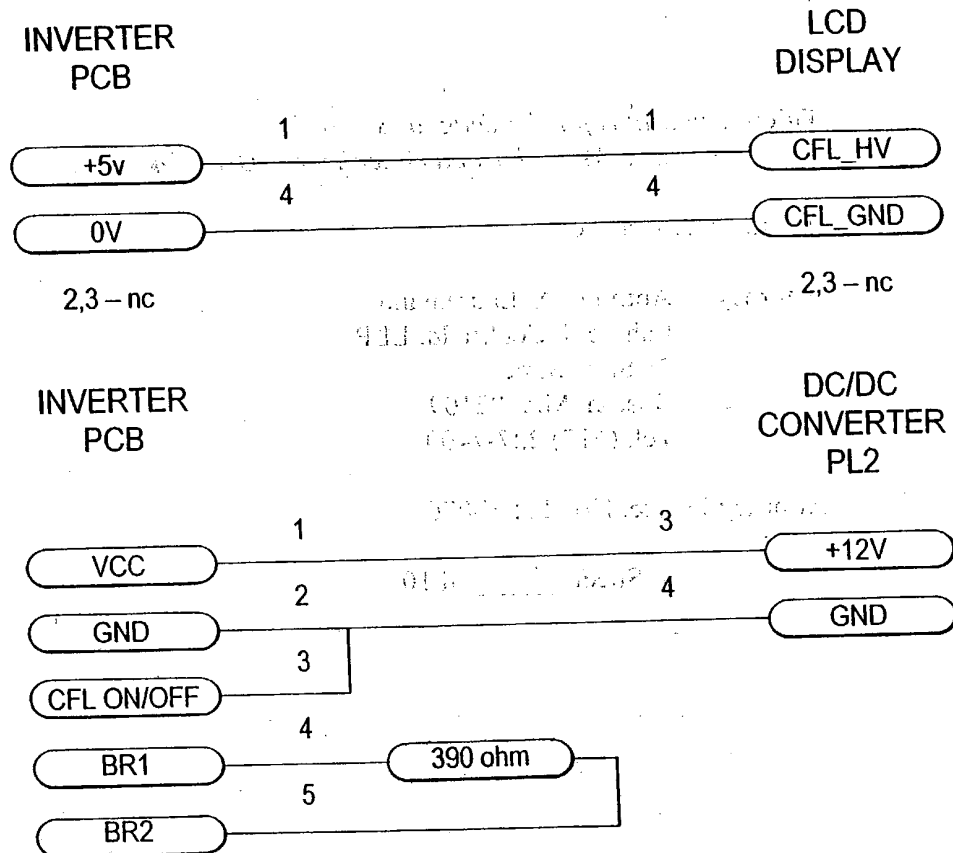
SPEAKER

GCAT  
J1**FIG. 3B**



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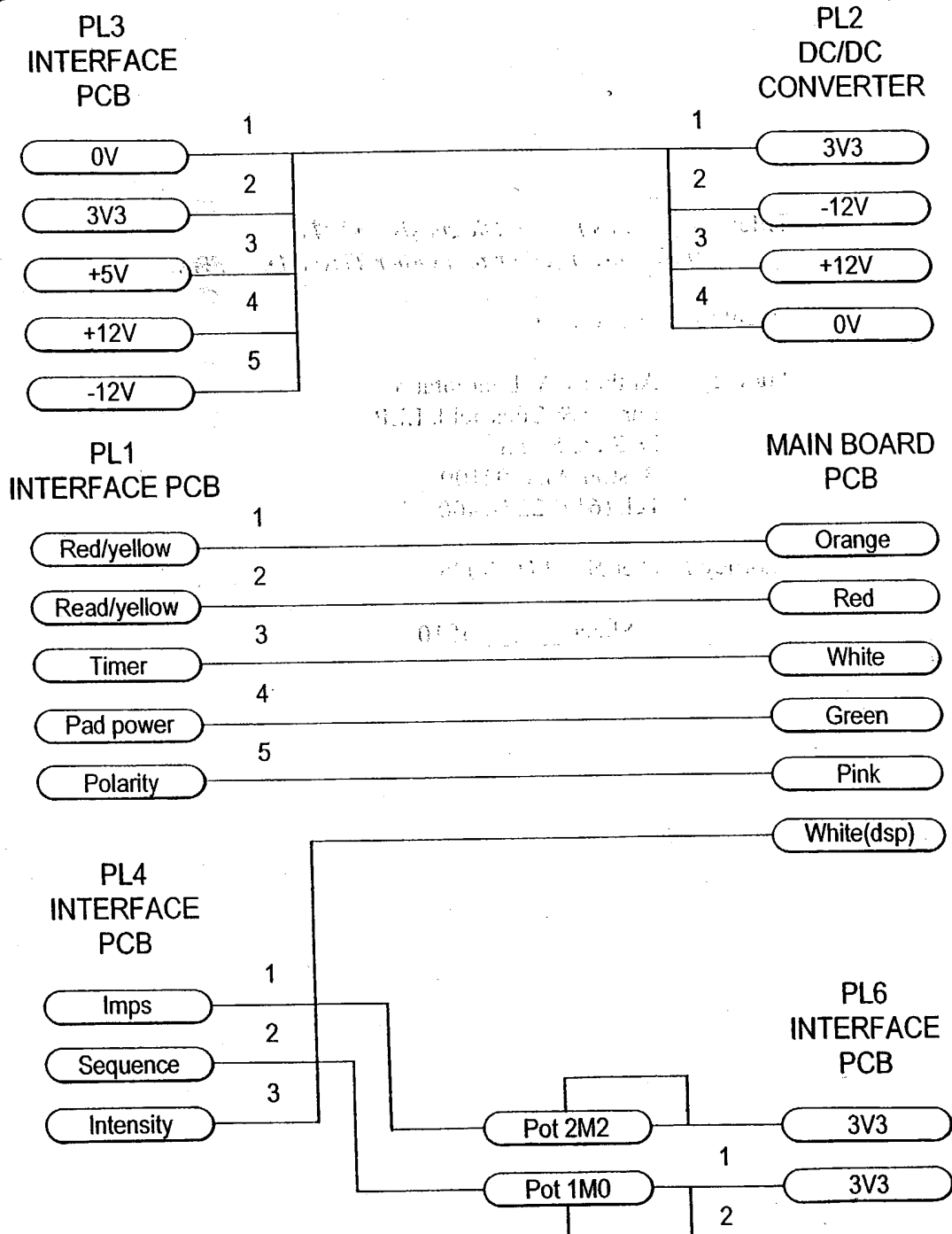


*FIG. 3C*



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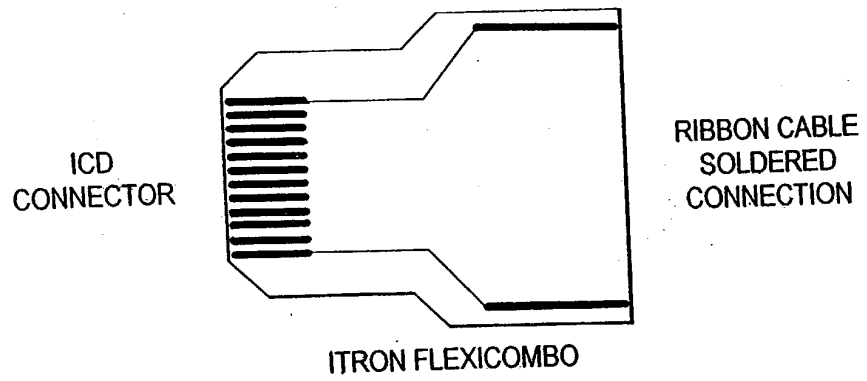
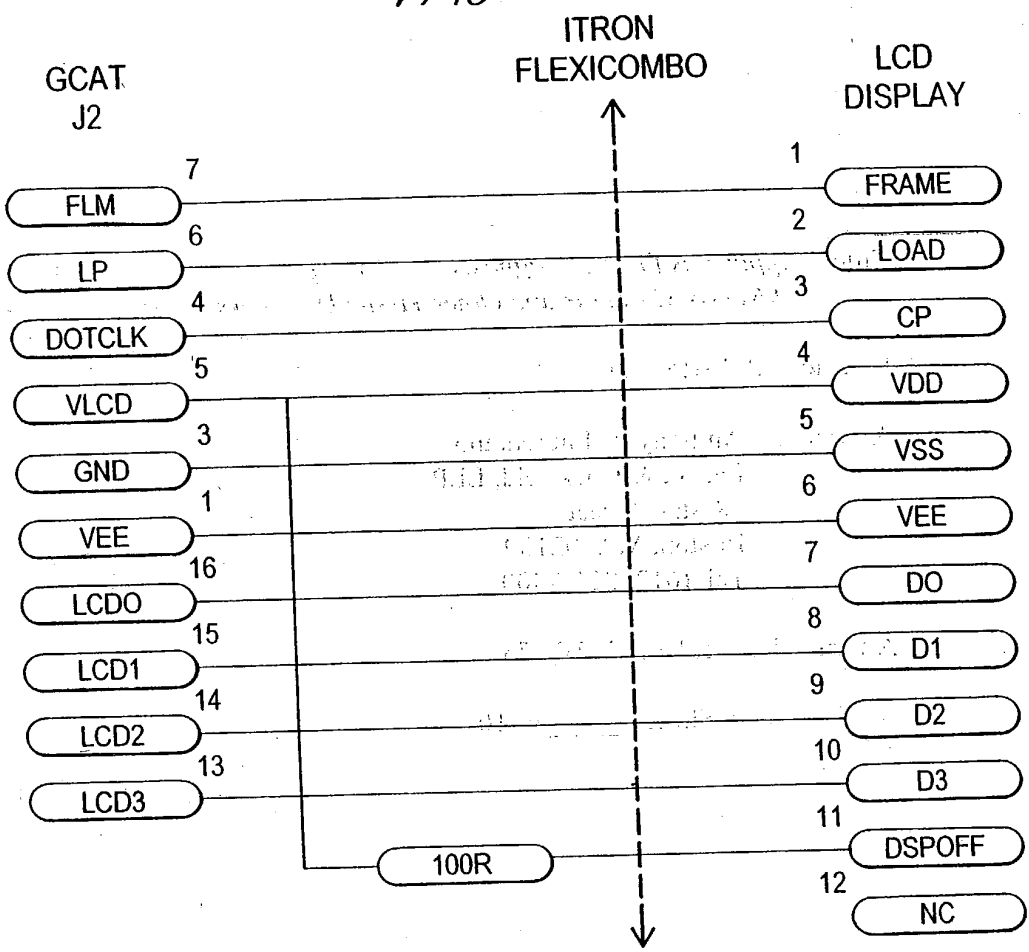


**FIG. 3D**

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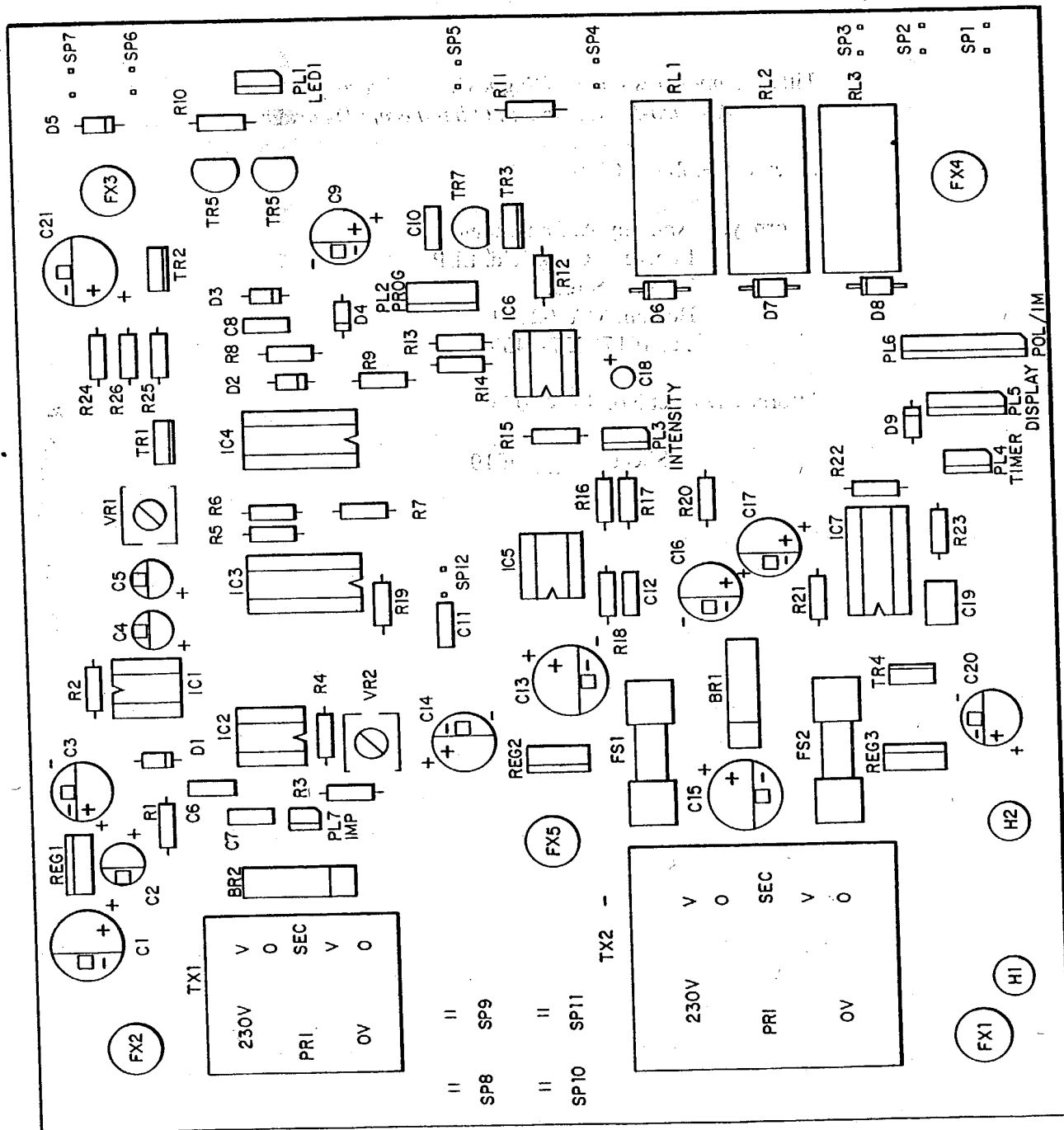
**FIG. 3E**



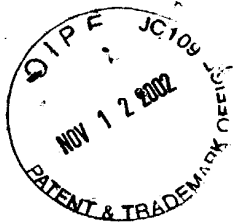
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FIG.  
4



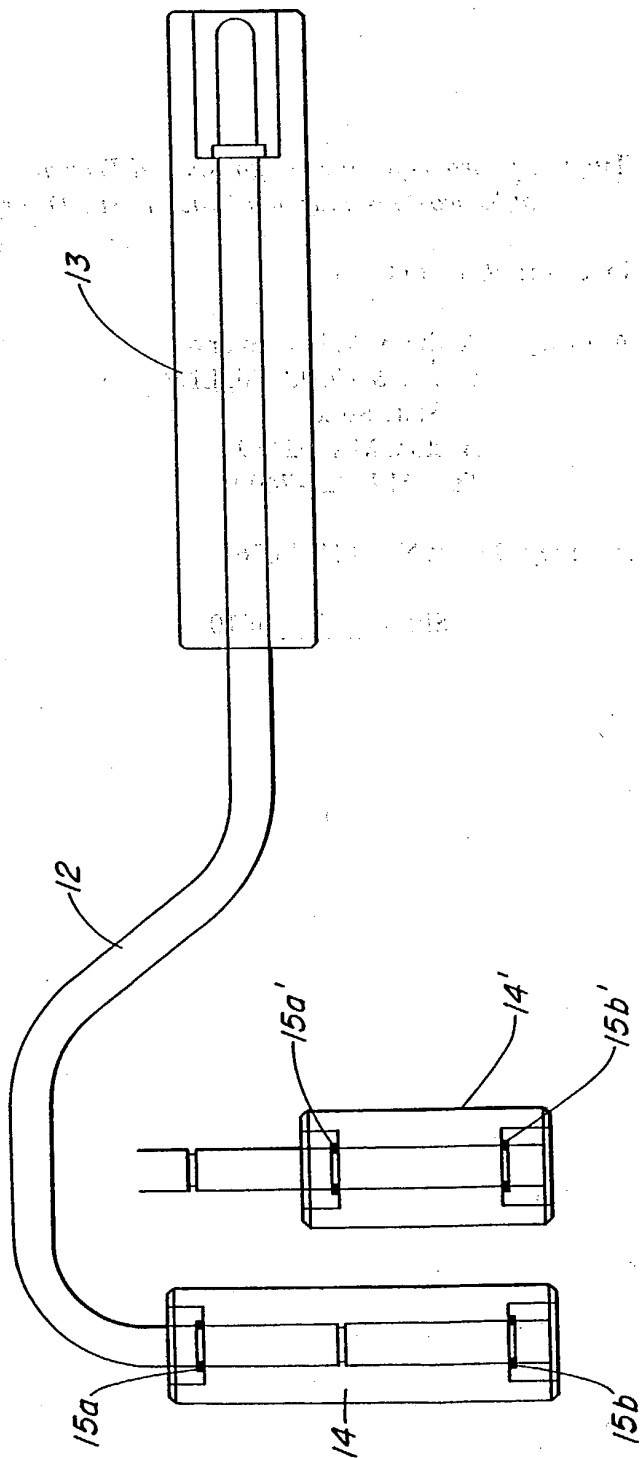




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FIG. 5



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FIG. 6

